PATENT Docket: 040003

IN THE SPECIFICATION

Please replace the following paragraphs in the Specification with the following rewritten paragraphs:

[0034] The two-modem design shown in FIG. 2 may be implemented in various manners. For example, each modem processor 210 may be implemented within one or more application specific integrated circuits (ASICs). Application—An application processor 240 may be implemented within a separate integrated circuit (IC) or incorporated within an ASIC for one of the modem processors. Similarly, a_main controller 250 may be implemented within as separate IC or incorporated within an ASIC for a modem processor. As another example, both modem processors 210a and 210b, application processor 240, and main controller 250 may be implemented within a single ASIC.

[0036] An application—Application processor 240 controls the operation of modem processors 210a and 210b and supports upper layer applications (e.g., for voice and packet data) that require the services of these modem processors. A main—Main—controller 250 controls the operation of various processing units at the multi-RAT device. Memories 242 and 252 store data and code used by controllers 240 and 250, respectively.